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APPLICANTS Michitaka Kameyama, Sendai-shi, JAPAN; Takahiro Hanyu, Sendai-shi, JAPAN; Hiromitsu Kimura, Ukyo-ku, JAPAN; Yoshikazu Fujimori, Ukyo-ku, JAPAN; Takashi Nakamura, Ukyo-ku, JAPAN; Hidemi Takasu, Ukyo-ku, JAPAN;						
** CONTINUING DATA ***** This application is a 371 of PCT/JP03/00568 01/22/2003						
** FOREIGN APPLICATIONS *****						
** IF REQUIRED, FOREIGN FILING LICENSE GRANTED **						
Foreign Priority claimed 35 USC 119(a-d) conditions met Verified and Acknowledged	<input checked="" type="checkbox"/> Yes <input type="checkbox"/> No <input checked="" type="checkbox"/> Yes <input type="checkbox"/> No /VIBOL TAN/ Examiner's Signature	<input checked="" type="checkbox"/> Met after Allowance VTan Initials	STATE OR COUNTRY JAPAN	SHEETS DRAWINGS 12	TOTAL CLAIMS 14	INDEPENDENT CLAIMS 5
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TITLE Logical operation circuit and logical operation method						
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